	LESSON PLAN	Date: 10.06.13
	Sub Name : DESIGN AND ANALYSIS OF ALGORITHMS Branch: B.Tech, IT	Semester: V

COURSE OBJECTIVES:

This course mainly focuses on various algorithm design techniques and how they are applied to solve various problems. Performance of every algorithm is evaluated. To Expertise the students with the various existing algorithm design techniques and to motivate them to design a new algorithms for various problems.

COURSE OUTCOMES:

Based upon above objectives the learning outcomes are defined as below. At the end of course students are familiar with

- Familiar with Key concepts: Definition of Algorithm, characteristics, Performance evaluation of algorithms.
- Familiar with Existing algorithm design techniques like Greedy method, Divide and Conquer, Dynamic Programming, Backtracking and Branch and Bound Methods.
- Able to apply algorithm design techniques to solve new problems.
- Able to design new algorithm design techniques for solving real world problems.
- Familiarity with solving NP-Hard problems.

LESSON PLAN:

Session No	Topics to be covered	Date	Teaching Method	Remarks
UNIT I				
1	Syllabus Presentation; Overview of the Subject	10.06.2013	BB	
2	Discussion of Course Objectives and Outcomes	11.06.2013	BB	
3	Algorithm Definition and Characteristics, Representations	12.06.2013	BB	
4	Time and Space Complexity	13.06.2013	BB	
5	Findig Time Complexity- Examples	14.06.2013	BB	
6	Findig Time Complexity- Examples	17.06.2013	BB	

7	Asymptotic Notations	18.06.2013	BB	
8	Divide and Conquer: Introduction and Basic Method	19.06.2013	BB	
9	Binary Search	19.06.2013	BB	
10	Finding Max.and Min element	20.06.2013	BB	
11	Merge Sort	21.06.2013	BB	
UNIT II				
12	The Greedy Method: Basic Method	24.06.2013	BB	
13	Knapsack Problem	25.06.2013	BB	
14	Tree vertex splitting	26.06.2013	BB	
15	Job sequencing with dead lines	27.06.2013	BB	
16	Minimum-cost spanning trees	28.06.2013	BB	
17	Kruskal's algorithm	29.06.2013	BB	
18	Optimal storage on tapes	01.07.2013	BB	
19	Optimal merge pattern	02.07.2013	BB	
20	Single Source Shortest Path Problem	03.07.2013	BB	
UNIT III				
21	Dynamic Programming: Basic Method	04.07.2013	BB	
22	Multistage graphs	05.07.2013	BB	
23	All pairs shortest path problem	08.07.2013	BB	
24	Single-source shortest path	09.07.2013	BB	
25	Optimal Binary search trees	10.07.2013	BB	
26	Optimal Binary search trees	11.07.2013	BB	
27	String Editing	12.07.2013	BB	
28	0/1 Knapsack	15.07.2013	BB	
29	Reliability design	16.07.2013	BB	
30	The Traveling salesman problem	17.07.2013	BB	
31	Flow shop scheduling problem	18.07.2013	BB	
UNIT IV				
32	Basic Traversal & Search Techniques: Techniques for Binary Trees	19.07.2013	BB	
33	Techniques for Graphs: BFS	22.07.2013	BB	
34	DFS	23.07.2013	BB	
35	Applications BFS & DFS	24.07.2013	BB	
36	Biconnected components	25.07.2013	BB	
37	Back Tracking: Basic Method	26.07.2013	BB	
38	The 8-Queens Problem	29.07.2013	BB	
39	Sum of subsets	30.07.2013	BB	
40	Graph coloring	31.07.2013	BB	
41	Hamiltonian cycle,	01.08.2013	BB	
42	Knapsack problem	02.08.2013	BB	
UNIT V				
43	Branch and Bound: Basic Method	05.08.2013	BB	
44	0/1 Knapsack problem	06.08.2013	BB	
45	Traveling salesperson	07.08.2013	BB	
46	Traveling salesperson	08.08.2013	BB	

47	NP hard and NP Complete Problems	12.08.2013	BB	
48	Basic concepts, Cook's Theorem	13.08.2013	BB	
49	NP-Hard Graph problems: Clique Decision Problem(CDP)	14.08.2013	BB	
50	Node Cover Decision Problem	16.08.2013	BB	
51	Chromatic Number Decision Problem	19.08.2013	BB	
52	AND/OR Graph decision problem	20.08.2013	BB	
53	NP Hard Scheduling Problems	21.08.2013	BB	
54	NP Hard Scheduling Problems	22.08.2013	BB	
55	Some NP-Hard Problems	23.09.2013	BB	
ADDITIONAL TOPICS BEYOND SYLLABUS				
56	Quick Sort	26.08.2013	BB	
57	Strassens Matrix Multiplication	27.08.2013	BB	
58	Randomized Algorithms	29.08.2013	LCD	
59	Genetic Algorithm	30.08.2013	LCD	
60	Applications of GA	31.08.2013	LCD	
REVISION OF SYLLABUS				
61	Revision of UNIT I	2.09.2013	BB	
62	Revision of UNIT II	3.09.2013	BB	
63	Revision of UNIT III	4.09.2013	BB	
64	Revision of UNIT IV	5.09.2013	BB	
65	Revision of UNIT V	6.09.2013	BB	


TEXT BOOK:

1. Ellis Horowitz, Sartaj Sahni, 'Fundamentals of Computer Algorithms', Galgotia Pubs.

REFERENCE BOOKS:

1. Aho, Hopcroft & Ullman, 'The Design and Analysis of Computer Algorithms', Addison Wesley.
2. Thomas H.Corman et al, 'Introduction to Algorithms', PHI.

	Prepared by	Approved by
Signature		
Name	Dr.K. Thirupathi Rao	HOD/IT
Designation	Professor & Dean, School of Computer Sciences	Professor
Date	01.06.2013	04.06.2013

	LESSON PLAN	Date: 10.06.13
	Sub Name : Theory of Computation Branch: B.Tech, IT	To 20.10.12 Page 01 of 06
	Semester: V	

LESSON PLAN			
	Sub: TOC		A.Y: 2013-14
S.NO	Date	TOPICS COVERED	REMARKS
1	10-06-2013	Fundamentals : Strings, Alphabet	
2	11-06-2013	Language, Operations, Finite state machine	
3	12-06-2013	definitions, finite automaton model	
4	13-06-2013	acceptance of strings, languages	
5	14-06-2013	deterministic finite automaton and non deterministic finite automaton,	
6	15-06-2013	transition diagrams and Language recognizers.	
7	17-06-2013	Finite Automata : NFA with ϵ transitions	
8	18-06-2013	Finite Automata : NFA with ϵ transitions - Significance, acceptance of languages.	
9	19-06-2013	Conversions and Equivalence : Equivalence between NFA with and without ϵ transitions	
10	20-06-2013	NFA to DFA conversion	
11	21-06-2013	minimization of FSM, equivalence between two FSM's,	
12	22-06-2013	Finite Automata with output- Moore and Melay machines.	
13	24-06-2013	Regular Languages : Regular sets	
14	25-06-2013	regular expressions, identity rules	
15	26-06-2013	Constructing finite Automata for a given regular expressions	
16	27-06-2013	Conversion of Finite Automata to Regular expressions	
17	28-06-2013	Pumping lemma of regular sets	
18	29-06-2013	closure properties of regular sets (proofs not required).	
19	01-07-2013	Grammar Formalism	
20	02-07-2013	Regular grammars-right linear and left linear grammars	
21	03-07-2013	equivalence between regular linear grammar and FA	
22	04-07-2013	inter conversion	
23	05-07-2013	Context free grammar,	
24	06-07-2013	derivation trees	
25	08-07-2013	sentential forms,	

26	09-07-2013&11-07-2013	Right most and leftmost derivation of strings	
27	12-07-2013&13-07-2013	Context Free Grammars : Ambiguity in context free grammars,	
28	15-07-2013&16-07-2013	Minimization of Context Free Grammars	
29	17-07-2013	Chomsky normal form	
30	18-07-2013&19-07-2013	Pumping Lemma for Context Free Languages.	
31	20-07-2013&22-07-2013	Push Down Automata	
32	23-07-2013	Push down automata, definition	
33	24-07-2013	model, acceptance of CFL	
34	25-07-2013&26-07-2013	Acceptance by final state and acceptance by empty state and its equivalence	
35	27-07-2013&29-07-2013	Equivalence of CFL and PDA	
36	30-07-2013&07-10-2013	Inter conversion	
37	08-10-2013&09-10-2013	Chomsky hierarchy of languages	
38	08-10-2013&09-10-2013	Chomsky hierarchy of languages and context sensitive language	
39	08-10-2013&09-10-2013	LR(0) grammar	
40	10-10-2013&11-10-2013	decidability problems,	
41	12-10-2013&14-10-2013	Universal Turing Machine	
42	15-10-2013&17-10-2013	NP complete and NP hard problems	
43	18-10-2013	Turing Machine	
44	19-10-2013&21-10-2013	definition,	
45	22-10-2013 &23-10-2013	Computable functions	
46	24-10-2013	recursively enumerable languages	
47	25-10-2013	Church's hypothesis	
48	26-10-2013	counter machine	
49	28-10-2013&29-10-2013	types of Turing machines	
50	30-10-2013&31-10-2013	design of TM	

51	01-11-2013	Tutorial	
52	04-11-2013	Revision of UNIT1	
53	05-11-2013	Revision of UNIT2	
54	06-11-2013	Revision of UNIT3	
55	07-11-2013	Revision of UNIT4	
56	08-11-2013	Revision of UNIT5	

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (Autonomous)

DEPARTMENT OF INFORMATION TECHNOLOGY
LESSON PLAN

Subject : OPERATING SYSTEMS

Course : B.Tech

Semester : V

Faculty :

Academic

year : Anupriya

Academic Year: 2013-14

S.NO		DATE	TOPIC TO BE COVERED	Hrs	TEACH. METHOD/ AID	Remarks
1	Week-1	06-10-2013	Introduction to computer components	1	Black Board	
2		06-11-2013	Computer System Organization	1	Black Board	
3		06-11-2013	Computer System Architecture	1	Black Board	
4		06-12-2013	Operating System Structure	1	Black Board	
5		13/6/2013	Operating System Operations	1	Black Board	
6	week 2	17/6/2013	Video tutorial on system assembling and installing different os	1	Black Board	
7		18/6/2013	Video tutorial on system assembling and installing different os	1	Black Board	
8		18/6/2013	Processes Management	1	Black Board	
9		19/6/2013	Memory Management	1	Black Board	
10		20/6/2013	Storage Management	1	Black Board	
11	week 3	24/6/2013	Protection and Security	1	Black Board	
12		25/6/2013	Protection and Security	1	Black Board	
13		25/6/2013	Distributed Sysems and Special Purpose Systems	1	Black Board	
14		26/6/2013	Tutorial-1	1		
15		27/6/2013	Operating System Services	1	Black Board	
16	week 4	07-01-2013	Operating System Services	1	Black Board	
17		07-02-2013	User Operating System Interface	1	Black Board	
18		07-02-2013	System Programs	1	Black Board	
19		07-03-2013	System Programs	1	Black Board	
20		07-04-2013	Operating System Design and Implementation	1	Black Board	
21	week 5	07-08-2013	Operating System Design and Implementation	1	Black Board	
22		07-09-2013	Virtual Machines	1	Black Board	

23		07-09-2013	Operating System Generation, System Boot	1	Black Board	
24		07-10-2013	Tutorial-2	1		
25		07-11-2013	Review of 1st unit	1	LCD	
26	week 6	15/7/2013	Introduction to processes	1	Black Board	
27		16/7/2013	Process Scheduling	1	Black Board	
28		16/7/2013	Operations on Processes	1	Black Board	
29		17/7/2013	Inter-Process Communication, Examples of IPC Systems	1	Black Board	
30		18/7/2013	Inter-Process Communication, Examples of IPC Systems	1	Black Board	
31	week 7	22/7/2013	Communication in Client-Server Systems	1	Black Board	
32		23/7/2013	Tutorial-3	1		
33		23/7/2013	Multithreading Models	1	Black Board	
34		24/7/2013	Thread Libraries	1	Black Board	
35		25/7/2013	Threading issues	1	Black Board	
36	week 8	29/7/2013	Scheduling criteria	1	Black Board	
37		30/7/2013	Scheduling Algorithms	1	Black Board	
38		30/7/2013	Multiple-Processor Scheduling	1	Black Board	
39		31/7/2013	Tutorial-4	1		
40		08-01-2013	Review of 2nd unit	1	LCD	
05/08/2013 to 07/08/2013 I-MID Examination						
41	week 9	08-08-2013	The Critical-Section Problem	1	Black Board	
42		08-12-2013	Peterson's Solution	1	Black Board	
43		13/8/2013	Synchronization Hardware	1	Black Board	
44		13/8/2013	Semaphores	1	Black Board	
45		14/8/2013	Classic Problems of Synchronization	1	Black Board	
46	week 10	19/8/2013	Monitors	1	Black Board	
47		20/8/2013	Tutorial-5	1		
48		20/8/2013	Synchronization Examples and Atomic Transactions	1	Black Board	
49		21/8/2013	System Model, Deadlock Characterization	1	Black Board	
50		22/8/2013	Methods for handling Deadlocks	1	Black Board	
51	week 11	26/8/2013	Deadlock Prevention	1	Black Board	
52		27/8/2013	Deadlock Avoidance	1	Black Board	
53		27/8/2013	Deadlock Detection and recovery	1	Black Board	
54		28/8/2013	Tutorial-6	1		


55	week 12	09-02-2013	Review of 3rd unit	1	LCD	
56		09-03-2013	Swapping, Contiguous Memory Allocation	1	Black Board	
57		09-03-2013	Paging Structure of the Page Table	1	Black Board	
58		09-04-2013	Segmentation	1	Black Board	
59		09-05-2013	Tutorial-7	1		
60	week 13	09-10-2013	Demand Paging	1	Black Board	
61		09-10-2013	Page replacement	1	Black Board	
62		09-11-2013	Allocation of Frames	1	Black Board	
63		09-12-2013	Thrashing	1	Black Board	
64	week 14	16/9/2013	Memory-Mapped Files	1	Black Board	
65		17/9/2013	Allocating Kernal Memory	1	Black Board	
66		17/9/2013	Tutorial-8	1		
67		18/9/2013	Review of 4th unit	1	LCD	
68		19/9/2013	The Concept of a File, Access Methods	1	Black Board	
69	week 15	23/9/2013	Directory Structure	1	Black Board	
70		24/9/2013	File System Mounting	1	Black Board	
71		24/9/2013	File sharing, Protection	1	Black Board	
72		25/9/2013	Tutorial-9	1	Black Board	
73		26/9/2013	File System Structure,Implementation	1	Black Board	
74	week 16	30/9/2013	Allocation Methods	1	Black Board	
75		10-01-2013	Free space Management	1	Black Board	
76		10-01-2013	Efficiency and Performance	1	Black Board	
77		10-03-2013	Recovery	1	Black Board	
78	week 17	10-07-2013	Tutorial-10	1		
79		10-08-2013	Review of 5th Unit	1	LCD	
80		10-08-2013	Overview of LINUX OPERATING SYSTEM	1	LCD	
81		10-09-2013	Features of LINUX,Introducing Android	1	LCD	
10/10/2013 to 12/10/2013 II-MID EXAMINATION						

TEXT BOOK :

1. Silberschatz & Galvin, 'OPERATING SYSTEM CONCEPTS', 7th edition, Wiley

REFERENCES :

1. William Stallings-"OPERATING SYSTEMS"-5th Edition-PHI
2. Charles Crowley, 'OPERATING SYSTEMS: A DESIGN-ORIENTED APPROACH', Tata McGraw Hill Co.,1998 edition
3. Andrew S.Tanenbaum, 'MODERN OPERATING SYSTEMS', 2nd edition,1995,PHI.

	LESSON PLAN	Date: 10.06.13
	Sub Name : SOFTWARE ENGINEERING Branch: B.Tech, IT	Semester: V

COURSE OBJECTIVES:

- An understanding of different software processes and how to choose between them
- How to understand requirements from a client and specify them
- Design in the large, including principled choice of software architecture, the use of modules and interfaces to enable separate development, and design patterns
- Understanding good code practices, including documentation, contracts, regression tests and daily builds.
- Various quality assurance techniques, including unit testing, functional testing and automated analysis tools.

COURSE OUTCOMES:

- Students will understand importance of software engineering and software process concepts.
- Students will learn about different software development process models and how to choose an appropriate one for project.
- Students will demonstrate the ability to manage a project including planning, scheduling and risk assessment/management.
- Students will gain confidence at having conceptualized, designed and implemented a working, medium sized project with their team.
- Students will learn about and go through the software development cycle with emphasis on different processes- requirements, design and implementation phases.

LESSON PLAN:

Session No	Topics to be covered	Date	Teaching Method	Remarks
UNIT I				
1	Introduction to software engineering	10.06.2013	BB	
2	The evolving role of Software	11.06.2013	BB	
3	software	12.06.2013	BB	
4	changing nature of software	13.06.2013	BB	
5	changing nature of software	14.06.2013	BB	
6	legacy software	17.06.2013	BB	
7	software myths.	18.06.2013	BB	
8	Tutorial	19.06.2013	BB	
9	Software process	19.06.2013	BB	
10	Assessment	20.06.2013	BB	
11	personal and team process models	21.06.2013	BB	

UNIT II				
12	Process models	24.06.2013	BB	
13	Prescriptive models	25.06.2013	BB	
14	water fall model	26.06.2013	BB	
15	Incremental	27.06.2013	BB	
16	Tutorial	28.06.2013	BB	
17	Evolutionary and specialized process models	29.06.2013	BB	
18	unified process	01.07.2013	BB	
19	Software engineering practice	02.07.2013	BB	
20	communication practices, planning practices	03.07.2013	BB	
UNIT III				
21	Requirements Engineering	04.07.2013	BB	
22	A bridge to design and construction	05.07.2013	BB	
23	RE tasks, initiating the RE process	08.07.2013	BB	
24	Eliciting Requirements	09.07.2013	BB	
25	developing use cases	10.07.2013	BB	
26	Building the analysis models	11.07.2013	BB	
27	Tutorial	12.07.2013	BB	
28	Requirements Engineering	15.07.2013	BB	
29	Negotiating and validating requirements.	16.07.2013	BB	
30	Building the analysis models	17.07.2013	BB	
31	Requirements analysis, Analysis modelling approaches	18.07.2013	BB	
UNIT IV				
32	Design Engineering	19.07.2013	BB	
33	Design within the context of software engineering	22.07.2013	BB	
34	Design process and software quality	23.07.2013	BB	
35	Design concepts	24.07.2013	BB	
36	Design mode	25.07.2013	BB	
37	pattern based software design	26.07.2013	BB	
38	Creating an architectural design:	29.07.2013	BB	
39	software architecture,	30.07.2013	BB	
40	Data design	31.07.2013	BB	
41	Architectural styles and patterns	01.08.2013	BB	
42	Architectural design	02.08.2013	BB	
UNIT V				
43	Testing Strategies	05.08.2013	BB	
44	A strategic to software testing	06.08.2013	BB	
45	strategic issues	07.08.2013	BB	
46	test strategies for conventional software	08.08.2013	BB	
47	Tutorial	12.08.2013	BB	
48	object oriented software	13.08.2013	BB	
49	validation testing	14.08.2013	BB	
50	system testing	16.08.2013	BB	
51	the art of debugging Testing tactics :	19.08.2013	BB	
52	software testing fundamentals	20.08.2013	BB	
53	Tutorial	21.08.2013	BB	

54	white box testing	22.08.2013	BB	
55	basis path testing,	23.09.2013	BB	
56	white box testing	26.08.2013	BB	
57	basis path testing,	27.08.2013	BB	
58	control structure testing	29.08.2013	LCD	
59	Black box testing,	30.08.2013	LCD	
60	OO testing methods	31.08.2013	LCD	
REVISION OF SYLLABUS				
61	Revision of UNIT I	2.09.2013	BB	
62	Revision of UNIT II	3.09.2013	BB	
63	Revision of UNIT III	4.09.2013	BB	
64	Revision of UNIT IV	5.09.2013	BB	
65	Revision of UNIT V	6.09.2013	BB	

TEXT BOOK

Roger S.Pressman, “Software engineering- A practitioner’s Approach”, McGraw-Hill International Edition, 2005, 6th edition.

REFERENCE BOOKS:

an Sommerville, “Software engineering”, Pearson education, 2008, 8th edition.

2. Ali Behforooz and Frederick J Hudson, “Software Engineering Fundamentals”, Oxford University Press, New Delhi, 1996.

3. Stephan Schach, “Software Engineering”, Tata McGraw Hill, 2007.

4. Pfleeger and Lawrence , “Software Engineering: Theory and Practice, Pearson education, 2001,1995, PHI, second edition.

	Prepared by	Approved by
Signature		
Name	K. SUBBARAO	HOD/IT
Designation	ASST. PROFESSOR	Professor
Date	01.06.2013	04.06.2013

LESSON PLAN

Faculty Name: B V N R SIVA KUMAR
 10/06/2013
 Course & Sem: B.TECH V SEM
 - 2014

Date:
 AY: 2013

MICROPROCESSOR AND INTERFACING

S.No.	DATE		TOPIC	Actu
1	10-Jun-13	UNIT I	Introduction - Microprocessor & Hexadecimal system	
2	11-Jun-13		Architecture - Registers	
3	13-Jun-13		Memory Segmentation, Registers usage	
4	14-Jun-13		Instruction Format, Addressing Mode	
5	15-Jun-13		MOV, XCHG instructions, Programs	
6	17-Jun-13		Addressing Modes	
7	18-Jun-13		Data Transfer Group, Programs	
8	20-Jun-13		Data Transfer Group, Flags	
9	21-Jun-13		Tutorial - 1	
10	22-Jun-13		ADD & ADC instructions	
11	24-Jun-13		Arithmetic Group, programs	
12	25-Jun-13		Arithmetic Group, programs	
13	27-Jun-13		Logical Group, Programs	
14	28-Jun-13		Tutorial - 2	
15	29-Jun-13		Logical Group, Programs	
16	1-Jul-13		String instructions, Programs	
17	2-Jul-13		Branching & Control group, Programs	
18	4-Jul-13		Branching & Control group, Programs	
19	5-Jul-13		Tutorial - 3	
20	6-Jul-13		Assembler directives	
21	8-Jul-13		Procedures & MACROs	
22	9-Jul-13	UNIT II	8086pin configuration, Memory interfacing	
23	11-Jul-13		8086pin configuration, I/O interfacing	
24	12-Jul-13		Tutorial - 4	
25	15-Jul-13		Timing diagram	
26	16-Jul-13		DMA Data Transfer	
27	18-Jul-13		8237 Block Diagram & Interfacing	
28	19-Jul-13		Tutorial - 5	
29	20-Jul-13	UNIT III	8255 PPI - Mode 0, Programs	
30	22-Jul-13		DAC Interfacing, Programs	
31	23-Jul-13		Stepper motors & Actuators	

32	25-Jul-13		Modes 1&2, ADC Interfacing		
33	26-Jul-13		Tutorial - 6		
34	27-Jul-13		Key Board & Display interfacing		
35	29-Jul-13		8279 Block diagram		
36	30-Jul-13		Revision - Unit I		
37	1-Aug-13		Revision - Unit II		
38	2-Aug-13		Revision - Unit II		
39	3-Aug-13		Revision - Unit III		
40	8-Aug-13		I Mid Paper Discussion		
41	12-Aug-13	UNIT IV	Interrupts, IVT		
42	13-Aug-13		Interrupt Response, DOS Interrupts		
43	16-Aug-13		Tutorial - 7		
44	17-Aug-13		PIC 8259, Modes		
45	19-Aug-13		Interfacing & Cascading		
46	20-Aug-13		Serial data Transfer		
47	22-Aug-13		8251 Architecture		
48	23-Aug-13		Tutorial - 8		
49	24-Aug-13		8251 Interfacing		
50	26-Aug-13		Data Transfer programs,		
51	27-Aug-13		USB		
52	30-Aug-13		Tutorial - 9		
53	31-Aug-13		UNIT V	80286 & 80386 Specifications	
54	2-Sep-13			Real & Protected mode	
55	3-Sep-13	Features			
55	5-Sep-13	paging			
56	6-Sep-13	Tutorial - 10			
57	7-Sep-13	Pentium features,			
58	10-Sep-13	Branch Prediction			
59	12-Sep-13	Overview			
60	13-Sep-13	Tutorial - 11			
61	16-Sep-13	8051 family specifications			
62	17-Sep-13	Architecture			
63	19-Sep-13	Pin configuration & Interfacing			
64	20-Sep-13	Tutorial - 12			
65	21-Sep-13	Timer operation			
66	23-Sep-13	Timer modes			
67	24-Sep-13	Serial Port			
68	26-Sep-13	Interrupt structure			
69	27-Sep-13	Tutorial - 13			

70	28-Sep-13		8051 Overview	
71	30-Sep-13		Revision - Unit I	
72	1-Oct-13		Revision - Unit II	
73	3-Oct-13		Revision - Unit III	
74	4-Oct-13		Revision - Unit IV	
75	5-Oct-13		Revision - Unit IV	
76	7-Oct-13		Revision - Unit V	
77	8-Oct-13		Revision - Unit V	

Signature of Faculty

Head of the Department

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (Autonomous)

DEPARTMENT OF Information Technology
LESSON PLAN

Subject : OPERATING SYSTEMS LAB

Course : B.Tech

Semester : V

Faculty :

Academic

year : Anupriya.K

Academic Year: 2013-14

W.No	DATE	TOPIC TO BE COVERED	Hrs	Batch	Remarks
Week-1	06-10-2013	Practice programs on C++	3	B1	
	06-12-2013	Practice programs on C++	3	B2	
week 2	17/6/2013	Simulate FCFS	3	B1	
	19/6/2013	Simulate FCFS	3	B2	
week 3	24/06/2013	Simulate SJF	3	B1	
	26/06/2013	Simulate SJF	3	B2	
week 4	07-01-2013	Simulate Priority scheduling	3	B1	
	07-03-2013	Simulate Priority scheduling	3	B2	
week 5	07-08-2013	Simulate RR scheduling	3	B1	
	07-10-2013	Simulate RR scheduling	3	B2	
week 6	15/7/2013	Simulate File allocation strategies Sequential, Indexed	3	B1	
	17/7/2013	Simulate File allocation strategies Sequential, Indexed	3	B2	
week 7	22/7/2013	Simulate File allocation strategies Linked	3	B1	
	24/7/2013	Simulate File allocation strategies Linked	3	B2	
week 8	29/7/2013	Simulate MVT and MFT	3	B1	
	31/7/2013	Simulate MVT and MFT	3	B2	
05/08/2013 to 07/08/2013 I-MID Examination					

week 9	08-12-2013	simulate file organisation techniques	3	B1	
	14/8/2013	simulate file organisation techniques	3	B2	
week 10	19/8/2013	simulate bankers algorithm for deadlock lock avoidance	3	B1	
	21/8/2013	simulate bankers algorithm for deadlock lock avoidance	3	B2	
week 11	26/8/2013	simulate bankers algorithm for deadlock lock avoidance	3	B1	
	28/8/2013	simulate bankers algorithm for deadlock lock avoidance	3	B2	
week 12	09-02-2013	simulate bankers algorithm for deadlock lock avoidance	3	B1	
	09-04-2013	simulate bankers algorithm for deadlock lock avoidance	3	B2	
week 13	09-11-2013	simulate page replacement algorithms	3	B2	
	16/9/2013	simulate page replacement algorithms	3	B1	
week 14	18/9/2013	simulate paging techniques of m/m management	3	B2	
	23/9/2013	simulate paging techniques of m/m management	3	B1	
week 15	25/9/2013	simulate paging techniques of m/m management	3	B2	
	30/9/2013	simulate paging techniques of m/m management	3	B1	
week 16	10-07-2013	LAB INTERNAL EXAM	3	B1	
	10-09-2013	LAB INTERNAL EXAM	3	B2	
01/10/2012 to 06/10/2012 II-MID EXAMINATION					

TEXT BOOK :

1. Silberschatz & Galvin, 'OPERATING SYSTEM CONCEPTS', 7th edition, Wiley

REFERENCES :

1. William Stallings-"OPERATING SYSTEMS"-5th Edition-PHI
2. Charles Crowley, 'OPERATING SYSTEMS: A DESIGN-ORIENTED APPROACH', Tata McGraw Hill Co.,1998 edition
3. Andrew S.Tanenbaum, 'MODERN OPERATING SYSTEMS', 2nd edition,1995,PHI.

Prepared By :

Head of the Department



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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Faculty Name: BVNR SIVA KUMAR, S S Reddy
TECHNOLOGY

Year: III B.Tech
INTERFACING LAB
Sem: V

DEPARTMENT OF INFORMATION
Date: 14-12-2013.
MICROPROCESSORS &

S.NO.	UNIT	DESCRIPTION	I Batch(MON)		II Batch(WED)		Signature
			Planned	Performed	Planned	Performed	
1.		Introduction to 8086 Kits & Debug	17-06-13		19-06-13		
2.		Programs on Data Transfer & Exchange	24-06-13		26-06-13		
3.		Programs on ADD,ADC,SUB	01-07-13		03-07-13		
4.		Program on Multi Byte ADD & SUB	08-07-13		10-07-13		
5.		Programs on MUL & DIV	15-07-13		17-07-13		
6.		Programs on code Conversion	22-07-13		24-07-13		
7.		Programs on Sorting, String	29-07-13		31-07-13		
8.		Programs on Subroutines, MASM	05-08-13		07-08-13		
9.		Study of 8259, Repetition of Expts	12-08-13		14-08-13		
10.	Cycle	DAC Interfacing- Generation of Waveforms	19-08-13		21-08-13		
11.	Cycle	ADC Interfacing	26-08-13		28-08-13		
12.	Cycle	Stepper Motor Interfacing	02-09-13		04-09-13		
13.	Cycle	Key Board Interfacing	17-09-13		19-09-13		
14.	Cycle	Display Interfacing	24-09-13		26-09-13		
15.	Cycle	8051 Program- Program & IO	01-09-13		03-09-13		
16.		INTERNAL EXAM	08-10-13		09-10-13		

Signature of the Faculty

HEAD OF THE DEPARTMENT, IT



Lakireddy Bali Reddy College of Engineering
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NAAC Accredited with "A" grade, Accredited by NBA, New Delhi &
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DEPARTMENT OF INFORMATION TECHNOLOGY

Subject: **Design And Analysis Of Algorithms Lab**

Course:

B.TECH V SEM

Lab Code: **P818**

Credits: **2**

Faculty: **S. Nagamani, T. Suresh, K.Purushottam**

A Y: **2013-14**

Notification Of cycles and schedules for conduction

S.No	CYCLE	DATE	List Of Programmes	SIGN
1	Cycle-1		To write a program and analyze Time Complexity and Space Complexity of MergeSort	
2			To write a program and analyze Time Complexity and Space Complexity of BinarySearch	
3	Cycle-2		To write a program and analyze Time Complexity and Space Complexity of Knapsack Problem	
4			To write a program and analyze Time Complexity and Space Complexity of Kruskal's Algorithm	
5			To write a program and analyze Time Complexity and Space Complexity of Prim's Algorithm	
6	Cycle-3		To write a program and analyze Time Complexity and Space Complexity of All Pair Shortest path	
7			To write a program and analyze Time Complexity and Space Complexity of Travelling Sales Person Problem	
8	Cycle-4		To write a program and analyze Time Complexity and Space Complexity of Graph coloring	
9			To write a program and analyze Time Complexity and Space Complexity of 8Queen Problem	
10	Cycle-5		To write a program and analyze Time Complexity and Space Complexity of NP-Graph hard Problem	
11			To write a program and analyze Time Complexity and Space Complexity of finding the maximum and minimum	

	LabWork Beyond Curriculum		element using Divide and Conquer Strategy	
12			Write a program to implement Dijkstra's Algorithm	
13	Practise Session		Practise Lab Cycles	
14	Exam		Internal Lab Examination	

Faculty Names:

Head Of the Department

S. Nagamani

T. Suresh

K. Purushottama Rao