



ABOUT THE INSTITUTE

The Lakireddy Bali Reddy College of Engineering (LBRCE), Mylavaram was established in the year 1998 by Lakireddy Bali Reddy Charitable Trust, whose architect is Er. Lakireddy Bali Reddy garu. The institute is established with the sole aim of providing high quality educational opportunities in the field of science, engineering, technology and management. It is spread over 60 acres of sprawling lush green landscape spotted with orchids and grooves. It is approved by AICTE, affiliated to JNTUK, Kakinada and attained autonomous status in the year 2010. It is accredited with NAAC and NBA (CSE, IT, ECE, EEE & ME) under Tier-I. A separate R&D cell is established in the college to focus on continuous sponsored research. It has various sponsored research projects funded by various funding agencies. At present, 8 B.Tech programs are offered.

- ❖ Aerospace Engineering
- ❖ Artificial Intelligence & Data Science
- ❖ Civil Engineering
- ❖ Computer Science Engineering
- ❖ Electrical & Electronics Engineering
- ❖ Electronics and Communication Engineering
- ❖ Information Technology
- ❖ Mechanical Engineering

Four M.Tech programs and M.B.A programme is offered. The M.Tech Programs are:

- ❖ Computer Science and Engineering
- ❖ Thermal Engineering
- ❖ Power Electronics and Drives
- ❖ VLSI & Embedded systems

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering was started in the year 1998. The Department is accredited by NBA (Tier-I). It is recognized as a Research centre by JNTUK Kakinada and research scholars are working under our research center. Received sponsored projects worth of Rs.1.5 crore from reputed R&D agencies. Faculty of the department is actively engaged in publishing papers in peer reviewed national and international journals and conferences.

COMMITTEE MEMBERS

Chief Patrons

Er. Lakireddy Bali Reddy

Chairman, LBRCE

Sri L. Jaya Prakash Reddy

Co-Chairman, LBRCE

Sri L.R.N.K. Prasad Reddy

Vice-Chairman, LBRCE

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President, LBRCE

Dr. K. Appa Rao, Ph. D

Principal, LBRCE

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Convener

Dr. Y. Amar Babu, Ph. D

HOD ECE, LBRCE

Coordinator

Dr. Srinivasulu Gundala, Professor,

Mobile Number: 9440831750

Co-coordinators

Mr. Venkata Rao G., Associate Professor,

Mobile Number: 9494535698

Mr. Sasi Bhushan K., Associate Professor,

Mobile Number: 9441824784

Organizing Committee

Dr E V Krishna Rao, Professor & Dean R&D

Prof. Bojja Ramesh Reddy, Professor & Coordinator, IQAC

Dr. A.Narendra Babu, Professor

Dr. P. Lachi Reddy, Professor

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Dr. T. Satyanarayana, Professor



**AICTE
Sponsored Online**

**Short Term Training Program on
“Mixed Signal Design Approaches for
Artificial Intelligence Processors”**



**Organized By
Department of
Electronics & Communication Engineering**

STTP DATES

Series-1: 28-12-2020 to 03-01-2021

Series-2: 18-01-2021 to 23-01-2021

Series-3: 01-02-2021 to 06-02-2021

**LAKIREDDY BALI REDDY COLLEGE OF
ENGINEERING
(AUTONOMOUS)**

(Accredited by NAAC & NBA (CSE, IT, ECE, EEE, MECH) under Tier-I
NIRF Ranked and ISO 9001:2015 Certified Institution
Approved by AICTE, New Delhi and Affiliated to JNTUK,
Kakinada

L. B. Reddy Nagar, Mylavaram, Krishna Dist.,
Andhra Pradesh - 521 230.

<https://www.lbrce.ac.in/>

ABOUT AICTE - STTP

All India Council for Technical Education (AICTE) was set up in November 1945 as a national-level apex advisory body to conduct a survey on the facilities available for technical education and to promote development in the country in a coordinated and integrated manner. And to ensure the same, as stipulated in the National Policy of Education (1986), AICTE was vested with: Short Term Training Program (STTP) intends to conduct faculty trainings through financial assistance from AICTE to enable faculty members in the field of technical education to introspect and learn techniques that can help prepare students for active and successful participants in a knowledge society.

VISION

Develop Technical Teachers who have the ability and passion to work wisely, creatively and effectively for the betterment of human kind.

MISSION

Impart technical knowledge, practical skills, leadership qualities, team spirit and ethical values. Motivate to combine the rigor of science, the power of engineering and thrill of learning. Inspire Research Scholars to be the knowledge seekers, novel solution providers to challenges.

RESOURCE PEOPLE

Eminent faculty from IITs, NITs R & D Institutions, and industry experts will provide theoretical and practical insights of STTP.

OBJECTIVE

The objectives of the training program entitled "Mixed Signal Design approach for AI Processors" alleviate the Design and analysis of CMOS Mixed signal Circuits like current sources, Current and Voltage reference circuits, Voltage converters, and Data Converters of AI Processors, and design issues allied with high performance Mixed Signal designs. It provides platform to enhance the skills towards Design and development of intelligent computational systems for the Teaching faculty.

CONTENT OF THE STTP

- Current sources
- Current and Voltage reference circuits
- Operational Amplifiers
- Voltage and Data converters
- Band gap Reference circuits
- Design of Op-Amps
- Phase locked loops
- Switch Capacitor Filters
- Frequency response and stability analysis of Op-Amp.
- Issues allied with high performance Mixed Signal designs.
- High Performance CMOS Op-Amp Design
- Mismatch Issues in Analog Layouts
- RF IC Design
- Intelligent agents
- Problem-solving through Search algorithm
- Knowledge Representation and Reasoning and Planning
- Representing and Reasoning with Uncertain Knowledge
- Decision-Making.
- Machine Learning and Knowledge Acquisition
- Logic Programming
- Deep Learning
- Reinforcement Learning

Outcome of the STTP

Participants can able to:

- Solve practical and state of the art analog IC design problems to serve VLSI industries.
- Apply mixed signal design approaches to solve complex problems and develop efficient AI Processors.
- Develop intelligent computational systems by assembling solutions to concrete computational problems.

INFORMATION TO PARTICIPANTS

- **No Registration fees.**
- This STTP is only for faculty members of the AICTE approved institutions, research scholars, participants from industry.
- The STTP will be conducted in online mode.
- Participants willing to participate in this online STTP should have relevant online gadgets viz., laptop/desktop/smart phone with adequate internet connectivity.
- The participants who have attendance 80% and more and also score minimum 60% in the online test will be issued certificates
- Intimation to the participants about registration confirmation shall be given through mail.

Registration Link:

<https://tinyurl.com/STTPMAIP>

IMPORTANT DATES

Last date for Registration: 25-12-2020

Intimation of Acceptance: 26-12-2020

ADDRESS FOR COMMUNICATION

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