

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

UG Time Table

Course/ Section: B.Tech V-Sem. A Section

Class Teacher: Mr.P.Venkata Rao

A.Y: 2020-21

W.E.F.:07/09/2020

DAY TIME	1		2		3		4		5
	9.30 to 10.30	10.30. to 10.40	10.40 to 11.40	11.40 to 11.50	11:50 to 12:50	12:50 to 1:50	1:50 to 2:50	2:50 to 3:00	3:00 to 4:00
MON	EES-I	B R E A K	COA	B R E A K	MEMS	L U N C H	VLSI	B R E A K	DC
TUE	DC		EMI		VLSI		DC Lab		TLW
WED	COA		MEMS		VLSI		VLSI Lab		EMI
THU	EMI		TLW		EEA		DC		MEMS
FRI	EEA		COA		TLW		MEMS		VLSI
SAT	EEA		DC		TLW		EMI		-----

S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EEA(3)	Engineering Economics and Accountancy	Mr.A.Adishesha Reddy(T104)
2.	17EC13	COA(3)	Computer Organization and Architecture	Dr.Y.Amar Babu(T059)
3.	17EC14	TLW(4)	Transmission Lines and Wave Guides	Mr.B.Siva Hari Prasad(T558)
4.	17EC15	DC(4)	Digital Communications	Mr.P.Venkata Rao(T657)
5.	17EC16	VLSI(4)	VLSI Design	Dr.G.Srinivasulu(T772)
6.	17EI18	MEMS(4)	Micro Electro Mechanical Systems(Program Elective-I)	Mr.G.Venkata Rao(T436)
7.	17EC90	EMI(4)	Electronic Measurements and Instrumentation (Add on course-I)	Mr.K.Sasi Bhusshan(T629)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.P.Venkata Rao(T657)/Mr.M.K.Linga Murthy/ Mr.B.P.Madhusudhana Rao
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Mr.G.Venkata Rao(T436)/Smt.T.Kalpana
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Smt.T.Radha Rani(T516)/K.Samaikya

Class Teacher
(Mr.P.Venkata Rao)

Time Table In-charge
(Mr.V.Ravi Sekhara Reddy)

HOD
(Dr.Y.Amar Babu)

Dean of Academics
(Dr.P.Lova Raju)

Principal
(Dr.K. Appa Rao)

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

UG Time Table

Course/ Section: B.Tech V-Sem. B Section

Class Teacher: Mr.K.Sasi Bhushan

A.Y: 2020-21

W.E.F.:07/09/2020

DAY TIME	1		2		3		4		5
	9.30 to 10.30	10.30. to 10.40	10.40 to 11.40	11.40 to 11.50	11:50 to 12:50	12:50 to 1:50	1:50 to 2:50	2:50 to 3:00	3:00 to 4:00
MON	EEA	B R E A K	DC	B R E A K	VLSI	L U N C H	VLSI Lab	B R E A K	MEMS
TUE	COA		VLSI		EES-I		MEMS		DC
WED	EEA		DC		EMI		DC Lab		TLW
THU	TLW		COA		VLSI		MEMS		EMI
FRI	EMI		TLW		MEMS		EEA		VLSI
SAT	COA		EMI		TLW		DC		-----

S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EEA(3)	Engineering Economics and Accountancy	Mr.U.Rambabu(T595)
2.	17EC13	COA(3)	Computer Organization and Architecture	Dr.Y.Amar Babu(T059)
3.	17EC14	TLW(4)	Transmission Lines and Wave Guides	Mr.V.Ravi Sekhara Reddy(T479)
4.	17EC15	DC(4)	Digital Communications	Dr.G.L.N.Murthy(T074)
5.	17EC16	VLSI(4)	VLSI Design	Dr.P.Lachi Reddy(T200)
6.	17EI18	MEMS(4)	Micro Electro Mechanical Systems(Program Elective-I)	Smt.T.Kalpana(T706)
7.	17EC90	EMI(4)	Electronic Measurements and Instrumentation (Add on course-I)	Mr.K.Sasi Bhushan(T629)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.Ch.Siva rama Krishna(T622)/ Mr.P.Venkata Rao/ Mr.B.P.Madhusudhana Rao
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Mr.K.Sasi Bhushan(T629)/Mr.G.Venkata Rao
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Mr.T.Bala Krishna(T804)/K.Samaikya

Class Teacher
(Mr.K.Sasi Bhushan)

Time Table In-charge
(Mr.V.Ravi Sekhara Reddy)

HOD
(Dr.Y.Amar Babu)

Dean of Academics
(Dr.P.Lova Raju)

Principal
(Dr.K. Appa Rao)

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

UG Time Table

Course/ Section: B.Tech V-Sem. C Section

Class Teacher: Smt.T.Kalpana

A.Y: 2020-21

W.E.F.:07/09/2020

DAY TIME	1		2		3		4		5
	9.30 to 10.30	10.30. to 10.40	10.40 to 11.40	11.40 to 11.50	11:50 to 12:50	12:50 to 1:50	1:50 to 2:50	2:50 to 3:00	3:00 to 4:00
MON	COA	B R E A K	MEMS	B R E A K	EEA	L U N C H	EMI	B R E A K	VLSI
TUE	COA		DC		MEMS		VLSI		TLW
WED	EES-I		COA		MEMS		EEA		DC
THU	EMI		TLW		DC		DC Lab		MEMS
FRI	TLW		DC		VLSI		VLSI Lab		EMI
SAT	EMI		VLSI		EEA		TLW		-----

S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EEA(3)	Engineering Economics and Accountancy	Mr.U.Rambabu(T595)
2.	17EC13	COA(3)	Computer Organization and Architecture	Mr.K.V.Ashok(T708)
3.	17EC14	TLW(4)	Transmission Lines and Wave Guides	Mr.V.Ravi Sekhara Reddy(T479)
4.	17EC15	DC(4)	Digital Communications	Dr.G.L.N.Murthy(T074)
5.	17EC16	VLSI(4)	VLSI Design	Dr.G.Srinivasulu(T772)
6.	17EI18	MEMS(4)	Micro Electro Mechanical Systems(Program Elective-I)	Smt.T.Kalpana(T706)
7.	17EC90	EMI(4)	Electronic Measurements and Instrumentation(Add on course-I)	Mr.B.V.N.R.Siva Kumar(T514)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.M.K.Linga Murthy(T513)/ Mr.Ch.Siva rama Krishna/ Mr.B.P.Madhusudhana Rao
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Smt.T.Kalpana(T706)/Mr.K.Sasi Bhushan
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Smt.Ch.Padma(T440)/K.Samaikya(T807)

Class Teacher
(Smt.T.Kalpana)

Time Table In-charge
(Mr.V.Ravi Sekhara Reddy)

HOD
(Dr.Y.Amar Babu)

Dean of Academics
(Dr.P.Lova Raju)

Principal
(Dr.K. Appa Rao)