



Memorandum of Understanding (MoU) for Industry Academia Collaboration

This Memorandum of Understanding ("MOU") for general cooperation is entered into an effective as of 17th October 2025 by and between:

Xtremesilica Pvt. Ltd., having its registered office at 494, Second floor, Chinmaya Mission Hospital Rd, opp. ICICI Bank, 1st Stage Indiranagar, Bengaluru, Karnataka 560038 hereinafter referred to as "COMPANY", which expression shall, unless it is repugnant to the context thereof be deemed to include all its successors and assignees represented by its board of Directors of the FIRST PARTY.

AND

Lakireddy Bali Reddy College of Engineering (LBRCE), Mylavaram, Andra Pradesh hereinafter referred to as "INSTITUTION" which expression shall, unless it is repugnant to the context thereof be deemed to include its successors and assignees represented by its Registrar of the SECOND PARTY.

COMPANY AND INSTITUTION shall hereinafter be individually referred to as "Party" and collectively referred to as the "Parties"

Now, therefore this MoU witnesses:

I - PURPOSE

Xtremesilica & Lakireddy Bali Reddy College of Engineering (LBRCE) hereby establish this memorandum of understanding [MOU] to express their desire to work together in areas of Semiconductor IP Core Development and capacity building activities.

Xtremesilica & LBRCE wish to explore a mutually beneficial relationship to:

- To foster excellence in the area of VLSI by the way of Collaboration between ECE Department of LBRCE, Bangalore and Xtremesilica Pvt. Ltd., Bangalore
- To establish a COE at LBRCE to promote joint Research, and Design & development of the Semiconductor IP Cores in the given technology areas
- To Promote and deliver Joint Paper publications and Patent filings to enable and establish leadership in the notified Technology



II - ROLES AND RESPONSIBILITIES

This MOU sets forth the general roles and responsibilities that the parties propose to follow as they cooperate in the exchange of information in order to collaborate further in the areas which are mutually beneficial to the charter and mission of both Xtremesilica & Bangalore Institute of Technology

The broad areas of engagement that have been identified as part of this general cooperation agreement are:

- A. Xtremesilica & LBRCE will endeavor to set a joint agenda on an annual basis in order to implementing the purpose
- B. Xtremesilica will work with LBRCE as Industry Partner as part of its R&D activities

COMPANY Responsibilities:

- A. Xtremesilica to support LBRCE as Industry Partner and COE Partner in VLSI and allied areas of mutual interest
- B. Xtremesilica to support LBRCE in framing/upgrading curriculum in the aforementioned technologies as industry partner if the need arises.
- C. Xtremesilica shall dedicate a SPOC for engagement from its side
- D. Xtremesilica to conduct selection process of students for the program at the beginning of each academic year and finalize the students list to be enrolled for the program
- E. Xtremesilica shall provide problem statement/s (Design Specifications) for Design of IP Cores in the beginning of the academic year along with timelines
- F. Xtremesilica to provide Interns the access to servers with commercial EDA Tool licenses during the IP development stage
- G. Xtremesilica to provide Internship certificate to the students who are selected to be a part of this program for the duration contributed
- H. Xtremesilica to support in Joint Paper publications and patent filing based on its merit and reviews
- I. Xtremesilica holds the right for IP verification and testing and further right to commercialization and filing of Patent.
- J. Xtremesilica reserves the right to pay the stipend/renumeration to faculty and students during the tenure of internship
- K. Xtremesilica to support short term courses/ training cum internship programs and/or Career Builder Programs for students to increase awareness on VLSI Technology



INSTITUTION Responsibilities:

1. LBRCE to upgrade curriculum as per the inputs from INDUSTRY partner and implement the same in delivery of courses in the curriculum
2. LBRCE can consider naming the COE facility as “VLSI LAB” .
3. LBRCE to be responsible for providing Hardware - PCs & networking – about 15 Systems, basic Operating systems software & pertinent equipment & software required for R&D program and FDP/Short Term Courses & Training cum Internship Programs at campus
4. LBRCE to provide to the center sufficient EDA tool suite for utilization towards Training. Hardware to include standard Evaluation boards.
5. LBRCE shall provide access to the conference proceedings/publications to its students based on the need by its faculty and students
6. LBRCE to encourage faculty and students to actively participate in the R&D program driven jointly.
7. LBRCE shall commit up to 6 students per annum for the R&D Activity starting 5th Semester B.Tech batch with a commitment to stay with the program until end of 8th Semester. Students starting 7th Sem may be considered depending on the availability of students of required competence and need.
8. LBRCE should ensure KT (Knowledge Transfer) is carried out by the outgoing students to the incoming batch of students to ensure seamless continuity of the program
9. LBRCE shall designate a faculty coordinator/SPOC to coordinate with COMPANY and propose faculty members for the R&D program
10. LBRCE shall not be paid any amount by Xtremesilica to carry out any of the R&D activities whatsoever
11. LBRCE shall propose the schedule of the student’s availability from 5th to 8th semester aligning with the University guidelines for award of internship certificate
12. LBRCE should keep a long-term view of this joint partnership towards gaining momentum each year and working towards outcomes at global standards



III Other Activities aligned to the partnership

- 1) LBRCE can conduct Webinar/workshop in association with Xtremesilica Technologies as per mutually agreed schedules and mode of operation
- 2) LBRCE can conduct paid Faculty Development Program (FDP), Short Term Training Programs (STTP), Training cum Internship Program, Career Builder Programs in association with Xtremesilica. These programs are aimed at providing industry skills in campus and boost competency and employability of students.
- 3) The cost of all training programs is decided mutually by INSTITUTION & COMPANY.
- 4) Xtremesilica shall support in conduct of Hardware Design Hackathon by the way of defining the problem statement, evaluation of outcome as well as active involvement during the conduction of the event. LBRCE shall be responsible for end-to-end conduction of the event at LBRCE Campus
- 5) Xtremesilica shall accommodate students from LBRCE for industrial visits and technical discussions at a mutually agreed conditions. The field visits to serve the industrial exposure to faculty and students of LBRCE

IV - INTELLETUAL PROPERTY

- a. The term "intellectual property" as used herein means all intellectual property, whether or not capable of being registered, including but not limited to patents, copyright, computer software, code, design, chip technology rights, mask works, trade secrets, know how technique, methodologies, trademark, service mark, logo, trade name and corporate names.
- b. Any inventions, discoveries, designs, or innovations resulting from the joint collaboration between LBRCE and Xtremesilica that are eligible for patent protection ("Joint Inventions") shall be jointly owned by both parties. The rights, title, and interest in any patent(s) filed or granted based on such Joint Inventions shall be shared by LBRCE and Xtremesilica, unless otherwise mutually agreed in writing. Both parties shall cooperate in good faith in the filing, prosecution, maintenance, and enforcement of such patents, and shall bear any associated costs in a manner to be mutually decided.



V - DATA SECURITY

- LBRCE shall ensure secure access of the COE area only to authorized personnel
- LBRCE shall maintain non-disclosure of the developed work under the COE program to the outside the two involved entities

VI - RELATIONSHIP

Lakireddy Bali Reddy College of Engineering (LBRCE) understands and acknowledges that its relationship with COMPANY or its affiliate will be that of an independent principal and nothing in this agreement is intended to or should be construed to create a partnership, joint venture, agency or employer-employee relationship and neither party shall have any authority of the other otherwise than as strictly provided herein

VII - TERMS AND TERMINATION

- 1) This MoU shall become effective from the date hereof and shall remain in force unless terminated in accordance with provisions for a duration of 2 years.
- 2) The MOU shall be renewed after every two years.
- 3) This MoU may be terminated by either party hereto if another party commits material breach or default in performance of its obligations hereunder and the same (if capable of being remedied), has not been cured within 30 (thirty) days of receipt of written notice of such breach or default.
- 4) In case of termination of agreement by Lakireddy Bali Reddy College of Engineering (LBRCE), the institution is obliged to give an undertaking stating that any part of training content IP including source content, illustrations, design case studies will not be used/reproduced in any format without prior written permission for any training related activities the institution might choose to offer and execute either independently or in collaboration with any other 3rd party. Violation of this may warrant legal implications subject to the discretion of Xtremesilica, Bangalore.
- 5) The termination hereof shall not serve to release a Party from the performance of such of its obligations as may have arisen prior to termination.

In WITNESS WHEREOF, the Parties have caused this MOU to be signed by their duly authorized representatives as of the MOU effective date above.



For LBRCE

For Xtremesilica Technologies Pvt. Ltd.

Name: Dr.K.Appa Rao

Name: Shaik Chand Basha

Designation: Principal

Designation: Director of Operations

Signature: 9.22

PRINCIPAL

Date: 17-10-2025
Sri Kalahasti College of Engineering and Technology
MYLAVARAM 521230

Signature: Sh. Chand Basha

Date: 17-10-2025



WITNESSES:

1. [Signature]
17/10/25
(Dr. V. SURYANARAYANA)

2. G. Srinivasulu
17/10/2025
Dr. G. Srinivasulu

[Signature]
[GIRISH B.V.]