LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

(Autonomous & Affiliated to JNTUK, Kakinada& Approved by AICTE, New Delhi,
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International Journals done by M.Tech Students Academic Year: 2018-2019

- Rajani Kumari, G. Srinivasulu "Design of Efficient 32-Bit Low Power Variable Latency Parallel Prefix Brent-Kung Adder" International Journal of Technical Innovation in Modern Engineering & Science, Vol.5, Issue.5, May 2019, pp: 485, ISSN: 2455-2585(UGC Approved Journal)
- 2. **Rajesh Ramisetti**, B.V.N.R. Siva Kumar, Y.Amar Babu "EFFICIENT ACCIDENT AVOIDING AND ADVANCED IDENTIFICATION SYSTEM" in *International Journal of Engineering in Advanced Research Science and Technology*, ISSN: 2350-0328Vol.2, Issue.3, PP-662-669, November-2018, H-Index-1
- 3. **Danala Madhuri**, Tella Anil Raju "Latency and power optimized AES Cryptography system using Scan Chain Reordering" in *International Journal of Engineering in Advanced Research Science and Technology*, ISSN: 2350-0328, Vol.2, Issue.3, PP-936-942,December-2018, H-Index-1
- 4. **K.Vamsi Krishna**, V.V. Rama Krishna, Y.Amar Babu "Reliable and Accurate target detection system using IOT" in *International Journal of Engineering in Advanced Research Science and Technology*, ISSN: 2350-0328, Vol.2, Issue.3, PP-670-680,November-2018, H-Index- 1 **DOI:** 10.14419/ijet.v7i4.17569
- 5. **Yaramala Madhavi,** Dr.Y. Amar Babu "DESIGN OF FFT PROCESSOR USING CONFLICT-FREE ADDRESS SCHEMES" International Journal of Research, Vol. VIII, Issue VI, June/2019,pp:5100-5104, ISSN:2236-6124.
- 6. **Poola Mounika,** Dr.P.Lachi Reddy "Design And Survey Of Scan Based Logic Bist (Built In Self Test)" International Journal of Research Vol. VIII, Issue VI, June/2019,pp:5112-5116, ISSN:2236-6124.