

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

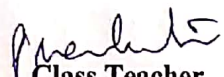
UG Time Table

Course/ Section: B.Tech V-Sem. A Section
Class Teacher: Mr.P.Venkata Rao


A.Y: 2020-21
W.E.F.:17/08/2020

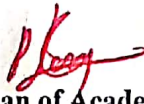
DAY	1		2		3		4
	10.00 to 11.00		11.00. to 11.15		11.15 to 12.15		12.15 to 01.15
MON	EES-I	B R E A K	VLSI	L U N C H	MEMS	B R E A K	COA
TUE	DC		EMI		VLSI		DC Lab
WED	COA		MEMS		VLSI		VLSI Lab
THU	EMI		TLW		EEA		DC
FRI	EEA		MEMS		TLW		COA
SAT	EEA		DC		TLW		EMI


S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EEA(3)	Engineering Economics and Accountancy	Mr.A.Adishesha Reddy(T104)
2.	17EC13	COA(3)	Computer Organization and Architecture	Dr.Y.Amar Babu(T059)
3.	17EC14	TLW(3)	Transmission Lines and Wave Guides	Mr.B.Siva Hari Prasad(T558)
4.	17EC15	DC(3)	Digital Communications	Mr.P.Venkata Rao(T657)
5.	17EC16	VLSI(3)	VLSI Design	Dr.G.Srinivasulu(T772)
6.	17EI18	MEMS(3)	Micro Electro Mechanical Systems(Program Elective-I)	Mr.G.Venkata Rao(T436)
7.	17EC90	EMI(3)	Electronic Measurements and Instrumentation (Add on course-I)	Mr.K.Sasi Bhushan(T629)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.P.Venkata Rao(T657)/Mr.M.K.Linga Marthy
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Mr.G.Venkata Rao(T436)/Smt.T.Kalpna
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Smt.T.Radha Rani(T516)/K.Samaikya


Class Teacher
(Mr.P.Venkata Rao)


Time Table In-charge
(Mr.V.Ravi Sekhara Reddy)


HOD
(Dr.Y.Amar Babu)


Dean of Academics
(Dr.P.Lova Raju)


Principal
(Dr.K. Appa Rao)


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
UG Time Table

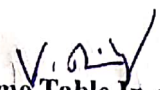
Course/ Section: B.Tech V-Sem. B Section
 Class Teacher: Mr.K.Sasi Bhushan


A.Y: 2020-21
 W.E.F.:17/08/2020

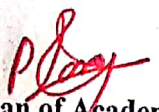
DAY	1		2		3		4
TIME	10.00 to 11.00	11.00. to 11.15	11.15 to 12.15	12.15 to 01.15	1:15 to 2:15	2:15 to 2:30	2:30 to 3:30
MON	EEA	B R E A K	DC	L U N C H	VLSI	B R E A K	VLSI Lab
TUE	COA		VLSI		EES-I		MEMS
WED	EEA		DC		EMI		DC Lab
THU	TLW		MEMS		VLSI		COA
FRI	EMI		TLW		MEMS		EEA
SAT	COA		EMI		TLW		DC

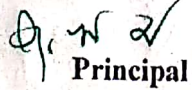
S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EEA(3)	Engineering Economics and Accountancy	Mr.U.Rambabu(T595)
2.	17EC13	COA(3)	Computer Organization and Architecture	Dr.Y.Amar Babu(T059)
3.	17EC14	TLW(3)	Transmission Lines and Wave Guides	Mr.V.Ravi Sekhara Reddy(T479)
4.	17EC15	DC(3)	Digital Communications	Dr.G.L.N.Murthy(T074)
5.	17EC16	VLSI(3)	VLSI Design	Dr.P.Lachi Reddy(T200)
6.	17E118	MEMS(3)	Micro Electro Mechanical Systems(Program Elective-I)	Smt.T.Kalpana(T706)
7.	17EC90	EMI(3)	Electronic Measurements and Instrumentation (Add on course-I)	Mr.K.Sasi Bhushan(T629)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.Ch.Siva rama Krishna(T622)/ Mr.P.Venkata Rao
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Mr.K.Sasi Bhushan(T629)/Mr.G.Venkata Rao
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Mr.T.Bala Krishna(T804)/K.Samaikya


Class Teacher
 (Mr.K.Sasi Bhushan)


Time Table In-charge
 (Mr.V.Ravi Sekhara Reddy)


HOD
 (Dr.Y.Amar Babu)


Dean of Academics
 (Dr.P.Lova Raju)


Principal
 (Dr.K. Appa Rao)

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

UG Time Table

Course/ Section: B.Tech V-Sem. C Section
 Class Teacher: Smt.T.Kalpana

A.Y: 2020-21
 W.E.F: 17/08/2020

DAY	1	B R E A K	2	L U N C H	3	B R E A K	4
	10.00 to 11.00		11.00. to 11.15		11.15 to 12.15		12.15 to 01.15
MON	COA		MEMS		EBA		EMI
TUE	COA		DC		MEMS		VLSI
WED	EES-I		COA		MEMS		EBA
THU	EMI		TLW		DC		DC Lab
FRI	TLW		DC		VLSI		VLSI Lab
SAT	EMI		VLSI		EBA		TLW

S.No.	Course Code	Course ID	Name of the Subject	Name of the Faculty
1.	17HS01	EBA(3)	Engineering Economics and Accountancy	Mr.U.Rambabu(T595)
2.	17EC13	COA(3)	Computer Organization and Architecture	Mr.K.V.Ashok(T708)
3.	17EC14	TLW(3)	Transmission Lines and Wave Guides	Mr.V.Ravi Sekhara Reddy(T479)
4.	17EC15	DC(3)	Digital Communications	Dr.G.L.N.Murthy(T074)
5.	17EC16	VLSI(3)	VLSI Design	Dr.G.Srinivasulu(T772)
6.	17EI18	MEMS(3)	Micro Electro Mechanical Systems(Program Elective-1)	Smt.T.Kalpana(T706)
7.	17EC90	EMI(3)	Electronic Measurements and Instrumentation(Add on course-1)	Mr.B.V.N.R.Siva Kumar(T514)
8.	17EC68	DC Lab(1)	Digital Communications Lab	Mr.M.K.Linga Murthy(T513)/ Mr.Ch.Siva rama Krishna
9.	17EC69	VLSI Lab(1)	VLSI Design Lab	Smt.T.Kalpana(T706)/Mr.K.Sasi Bhushan
10.	17PD05	EES-I(1)	Employability Enhancement Skills-I	Smt.Ch.Padma(T440)/K.Samaikya(T807)

T. Kalpana
Class Teacher
 (Smt.T.Kalpana)

V. Ravi Sekhara Reddy
Time Table In-charge
 (Mr.V.Ravi Sekhara Reddy)

Y. Amar Babu
HOD
 (Dr.Y.Amar Babu)

P. Lova Raju
Dean of Academics
 (Dr.P.Lova Raju)

K. Appa Rao
Principal
 (Dr.K. Appa Rao)