

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING



Electronics and Communication Engineering

PG TIME TABLE

Course/ Section : M.Tech(VLSI&ES), II Sem.
 Class Room : 4G02
 Class Teacher : Mrs.M.Ramya Harika

A.Y : 2023-24
 W.E.F : 12-02-2024

DAY TIME	1	2		3	4		5	6	7	7
	9.00 to 9.50	9.50 to 10.40	10.40 to 10.50	10.50 to 11.40	11.40 to 12.30	12.30 to 1.30	1.30 to 2.20	2.20 to 3.10	3.10 to 4.00	4.00 to 5.00
MON	AVLSI	CPLD	B R E A K	ASICD	CI	L U N C H	RTOS	RTOS	SL	Sports/ Yoga
TUE	RTOS	CPLD		AVLSI	ASICD		AVLSI Lab			
WED	CI	AVLSI		CPLD	ASICD		RTOS Lab			
THU	RTOS	AVLSI		ASICD	CPLD		Mini Project			

S.No	Course code	Course ID	Name of the Subject / Lab	Name of the Faculty
1	23VE09	AVLSI(4)	Analog VLSI Design	Dr.K.Rani Rudrama(T549)
2	23VE10	RTOS(4)	Real Time Operating Systems	Mrs.M.Ramya Harika(T631)
3	23VE11	CPLD(4)	CPLD and FPGA Architectures	Mrs.T.Kalpana(T706)
4	23VE15	ASICD(4)	ASIC Design	Dr.G.Srinivasulu(T772)
5	23AC05	CI(2)	Constitution of India	Mr.Ch.Sivarama Krishna(T622)
6	23VE63	AVLSI Lab(3)	Analog VLSI Design Lab	Dr.K.Rani Rudrama(T549)
7	23VE64	RTOS Lab(3)	Real Time Operating Systems Lab	Mrs.M.Ramya Harika(T631)
8	23PI01	Mini Project(3)	Mini Project	Mr.K.Sasi Bhushan(T629)
9		SL(1)	Self-Learning (Library, Swayam)	Mrs.M.Ramya Harika(T631)

Class Teacher
 Mrs.M.Ramya Harika

Time Table In-charge
 V.RaviSekhara Reddy

HOD
 Dr.Y.AmarBabu

Dean of Academics
 Dr.M.Srinivasa Rao

Principal
 Dr.K.Appa Rao