

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING



Electronics and Communication Engineering


PG TIME TABLE


Course/ Section : **M.Tech(VLSI&ES), I Sem.**
 Class Room : **4G02**
 Class Teacher : **Mrs.B.Rajeswari**


A.Y : **2023-24**
 W.E.F : **04-10-2023**

DAY TIME	1	2		3		4	5	6	7
	9.00 to 10.00	10.00 to 11.00	11.00 to 11.10	11.10 to 12.10	12.10 to 1.10	1.10 to 2.10	2.10 to 3.10	3.10 to 4.10	4.10 to 5.00
MON	DVLSI	ERPW	B R	IVP	L U N C H	ESD	Library		Sports/ Yoga
TUE	DVLSI	RM&IPR		CNS		ESD Lab			
WED	Digital VLSI System Design Lab			DVLSI		RM&IPR	CNS		
THU	ESD	ERPW	E A K	RM&IPR		IVP	Library		
FRI	IVP	CNS		ERPW		ESD	Library		


S.No	Course ID	Name of the Subject / Lab	Name of the Faculty
1	DVLSI	Digital VLSI System Design	Mr.K.Sasi Bhushan(T629)
2	ESD	Embedded System Design	Mr.M.K.Linga Murthy(T513)
3	IVP	Image and Video Processing	Mrs.B.Rajeswari(T519)
4	CNS	Cryptography and Network Security	Dr.K.Ravi Kumar
5	RM&IPR	Research Methodology and IPR	Dr.B.Poornaiah
6	ERPW	English for Research paper Writing	Dr.M.V.Sudhakar(T195)
7	Digital VLSI System Design Lab	Digital VLSI System Design Lab	Mr.K.Sasi Bhushan(T629)
8	ESD Lab	Embedded System Design Lab	Mr.M.K.Linga Murthy(T513)


 Class Teacher
 B.Rajeswari


 Time Table In-charge
 V.RaviSekhara Reddy


 HOD
 Dr.Y.AmarBabu


 Dean of Academics
 Dr.M.Srinivasa Rao


 Principal
 Dr.K.Appa Rao