<table>
<thead>
<tr>
<th>S.No.</th>
<th>H.T.No.</th>
<th>20TE01</th>
<th>SGPA</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>20761D2101</td>
<td>B</td>
<td>7.00</td>
</tr>
</tbody>
</table>

Exam Month & Year: Jun 2022

Date: 5 JUL 2022

Verified by 1: K. GM
Verified by 2: K. GPR

Controller of Examinations

Principal

Note: Any discrepancy noted should be brought to the notice of C.o.E. immediately.
# Results Sheet

**Programme:** M.Tech. (I Semester) (R20) Supplementary  
**Specialization:** VLSI and Embedded Systems  
**Exam Month & Year:** Jun 2022

## Course Code & Name (Theory)

1. 20VE01 - Digital VLSI System Design

## Table

<table>
<thead>
<tr>
<th>S.No.</th>
<th>H.T.No.</th>
<th>20VE01</th>
<th>SGPA</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>20761D6801</td>
<td>B</td>
<td>8.17</td>
</tr>
</tbody>
</table>

---

**Date:** 5 JUL 2022  
**Verified by:** 1: [Signature]  2: [Signature]  
**Note:** Any discrepancy noted should be brought to the notice of C.o.E. immediately.

**Controller of Examinations:** [Signature]  
**Principal:** [Signature]