



# LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)

L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P  
Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada

## M.Tech.(II Semester) (R20) II Mid Examination (2020 Admitted batch)

### TIME TABLE

Time : 02.00 PM to 04.00 PM

A.Y. 2020-21

Date	VLSI and Embedded Systems	Thermal Engineering
27-09-2021 (Monday)	20VE09 - Analog VLSI Design	20TE09 - Computational Fluid Dynamics
29-09-2021 (Wednesday)	20VE10 - Real Time Operating Systems	20TE10 - Renewable Energy Technology
01-10-2021 (Friday)	PE-III 20VE11 - CPLD and FPGA Architectures	PE-III 20TE11 - Thermal Measurement and Process Control
04-10-2021 (Monday)	PE-IV 20VE15 - ASIC Design	PE-IV 20TE14 - Hybrid Electric Vehicles

Note: Any omissions or clashes in this time table may please be informed to the Controller of Examinations immediately.

Date: 17-09-2021

  
CONTROLLER OF EXAMINATIONS

  
PRINCIPAL

Copy to:

1. Vice-Principal, Deans & HoDs
2. Transport in-charge & Librarian
3. Canteen, Security & Hostels
4. All Notice Boards