



**LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING
(AUTONOMOUS)**

L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P
Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada

**M.Tech.(II Semester) (R20) I Mid Examination
(2020 Admitted batch)**

TIME TABLE

Time : 09.30 AM to 11.30 AM

A.Y. 2020-21

Date	VLSI and Embedded Systems	Thermal Engineering
18-09-2021 (Saturday)	20VE09 - Analog VLSI Design	20TE09 - Computational Fluid Dynamics
20-09-2021 (Monday)	20VE10 - Real Time Operating Systems	20TE10 - Renewable Energy Technology
21-09-2021 (Tuesday)	PE-III 20VE11 - CPLD and FPGA Architectures	PE-III 20TE11 - Thermal Measurement and Process Control
22-09-2021 (Wednesday)	PE-IV 20VE15 - ASIC Design	PE-IV 20TE14 - Hybrid Electric Vehicles

Note: Any omissions or clashes in this time table may please be informed to the Controller of Examinations immediately.

Date: 11-09-2021


CONTROLLER OF EXAMINATIONS


PRINCIPAL

- Copy to:
1. Vice-Principal, Deans & HoDs
 2. Transport in-charge & Librarian
 3. Canteen, Security & Hosteis
 4. All Notice Boards